

**ADE4INLANG (10/100/1000)**  
4-port Intel LAN Expansion  
GEN2 Daughter Board



Dimension: 75 x 82 mm

Chipset: Intel 82574L

Features:

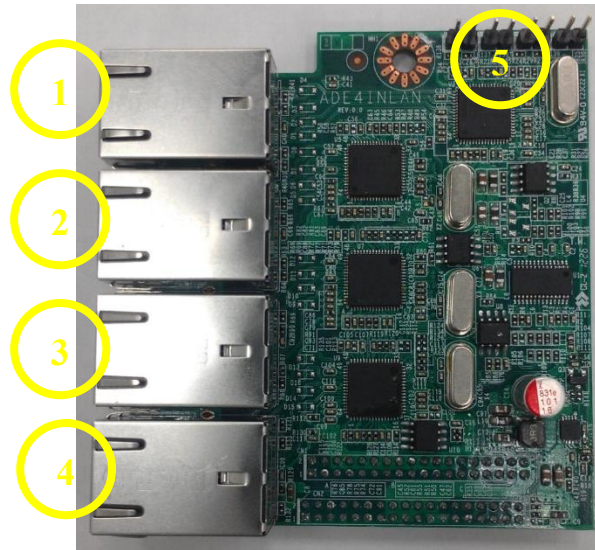
- Programmable host memory receive buffers (256 bytes to 16 KB)
- Intelligent interrupt generation features to enhance driver performance
- Message Signaled Interrupts (MSI and MSI-X)
- Configurable receive and transmit data FIFO, programmable in 1KB increments
- Flow Control Support compliant with the 802.3X Specification
- VLAN support compliant with the 802.1Q Specification
- Compliant with the 1Gb/s IEEE 802.3 802.3u 802.3ab

Specifications:

- IEEE 802.3ab auto negotiation support
- Full duplex operation at 10/100/1000 Mb/s
- Half duplex at 10/100 Mb/s
- Auto MDI, MDI-X crossover at all speeds
- TCP segmentation capability compatible with Large Send offloading features
- Support up to 256 KB TCP segmentation (TSOv2)
- Fragmented UDP checksum offload for packet reassemble
- IPv4 and IPv6 checksum offload support (receive, transmit, and large send)

- Receive Side Scaling (RSS) with two hardware receive queues
- 9 KB jumbo frame support
- 40 KB packet buffer size
- NC-SI for remote management core
- Magic Packet\* wake-up enable with unique MAC address
- ACPI register set and power down functionality supporting D0 and D3 states
- Full wake up support (APM and ACPI 2.0)
- Smart power down at S0 no link and Sx no link

**Connectors & Pin Definition :**



1 ~ 4 : LANA ~ LAND

5 : LANA\_LED ~ LAND\_LED

Compatibility: (includes I/O shields for these MB)

- NF9N-2930, NF9G-QM77, NF9I-2550



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